K ANIL KUMAR

M.Tech (Optoelectronics and Laser Technology)
School of Studies in Electronics and Photonics
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Date of Birth : 07 April 1993 | **Gender** : Male | **Nationality** : Indian | **Contact** : +91-7898301229

PERSONAL STATEMENT

"As a third semester M.Tech student in optoelectronics and Laser technology, I am seeking a position for a project internship for my major project, in your esteem institute, where as an intern, I can explore career paths in the field of semiconductor and optoelectronics device design and fabrication.

I'm currently working on grating modeling using GD-Calc , which aims at improving light absorption by designing the solar cell in such a way that the path length of light inside the photo active material is as long as possible.

I'm flexible and open to learning, from you and other Mentors/ Scientist in the field."

EDUCATION

2020-Pursuing
    M.Tech, Optoelectronics and Laser technology,
    School of Studies in Electronics and Photonics, PRSU, Raipur, Chhattisgarh
    90.14 % in First Semester, Awaiting for Second Semester result

2012-2016
    B.E, Electronics and telecommunication,
    Government Engineering College, Raipur, Chhattisgarh
    63.96 %

AISSCE-2011 (Class XII)
    Kendriya Vidyalaya, Raipur, Chhattisgarh
    74.8 %

AISSE – 2009 (Class X)
    Kendriya Vidyalaya, Raipur, Chhattisgarh
    80.6 %

DIGITAL TOOLS / SKILLS

C/C++ | Matlab | Octave | Python 3 | Quirk | Qiskit | Microsoft Office (Word, Power Point, Excel)

GD-Calc | Xilinx | VHDL | HTML | CSS

WORK EXPERIENCE / INTERNSHIPS

June 2015 – July 2015 : Vocational Trainee, Airports Authority of India
Air Traffic Control (ATC), CNS and Security
Swami Vivekananda International Airport, Raipur, Chhattisgarh.

August 2014 – September 2014 : **Vocational Trainee, Steel Authority of India**
Signaling and telecommunications within a Plant, Plant safety, Basic knowledge on working of different production mills and shops within a Plant
Bhilai Steel Plant, Bhilai, Chhattisgarh.

ACOMMPLISHED PROJECTS

Major Project: Feb 2016 – May 2016
"Traffic Control System for Emergency Vehicles"
Project description:
The main aim of this project is to develop an intelligent ambulance which will reach the hospitals without any problem in heavy traffics. We introduced a scheme called ITLS (Intelligent Traffic Light System), which would control mechanically the traffic lights in the path of the ambulance.

Minor Project: Oct 2015 – Dec 2015
"Automatic Speed Control of Vehicles using VHDL"
Project description:
In this project, a way of automatic speed control (ASC) of vehicles in speed limiting zones is analyzed using VHDL models and coding. VHDL is an acronym for VHSIC (very high speed integrated circuit) Hardware Descriptive Language.

AREA OF INEREST

Solar PV Technology | Quantum Communication | Quantum Computing | Photonic integrated circuits
Optoelectronics device design and modelling

LANGUAGES

Telugu | Hindi | English